

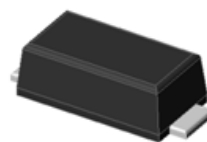
1A,30V Schottky Barrier Rectifier

Features

- Low leakage current
- Schottky barrier diode
- Low forward voltage drop
- Moisture sensitivity: level 1, per J-STD-020
- Halogen-free according to IEC 61249-2-21 definition
- High temperature soldering guaranteed: 260°C/10 seconds



RoHS
COMPLIANT



eSGA (SOD-123FL)

Applications

For use in low voltage, high frequency inverters, free-wheeling and polarity protection application.

Maximum Ratings & Electrical Characteristics (T _A =25°C unless otherwise noted)			
Parameter	Symbol	FSL13	Unit
Maximum repetitive peak reverse voltage	V _{RRM}	30	V
Maximum RMS voltage	V _{RMS}	21	V
Maximum DC blocking voltage	V _{DC}	30	V
Maximum average forward rectified current	I _{F(AV)}	1	A
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load	I _{FSM}	40	A
Operating junction temperature range	T _J	-55 to +125	°C
Storage temperature range	T _{STG}	-55 to +150	°C

Thermal-Mechanical Specifications (T _A =25°C unless otherwise noted)			
Parameter	Symbol	Typ	Unit
Thermal Resistance, Junction to Ambient	R _{thJA}	100	°C /W
Thermal Resistance, Junction to Case	R _{thJC}	20	°C /W
Thermal Resistance, Junction to Lead	R _{thJL}	20	°C /W

Electrical Specifications($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Typ	Max	Unit
Forward drop voltage	V_F	$I_F=0.5\text{A}$	0.35	-	V
		$I_F=1\text{A}$	0.38	0.42	
Reverse leakage current @ V_R	I_R	$T_J=25^{\circ}\text{C}$	0.067	0.50	mA
		$T_J=100^{\circ}\text{C}$	5.28	30	
Junction capacitance	C_J	$V_R=4.0\text{V}$, $f=1\text{MHz}$	85	-	pF

Note:

1. Mounted on copper pad area of 0.2x0.2" (5.0 x 5.0mm) to each terminal.

Ratings and Characteristics Curves ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

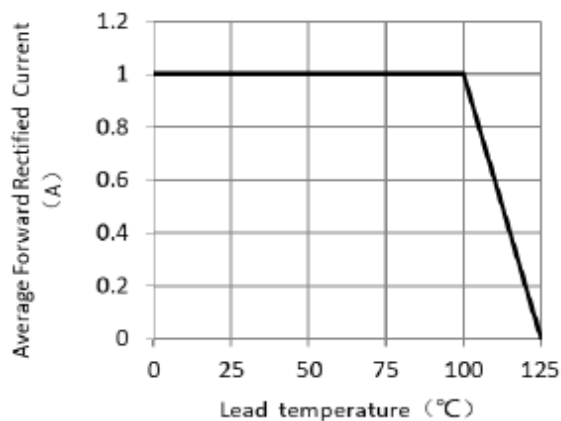


Figure 1. Forward Current Derating Curve

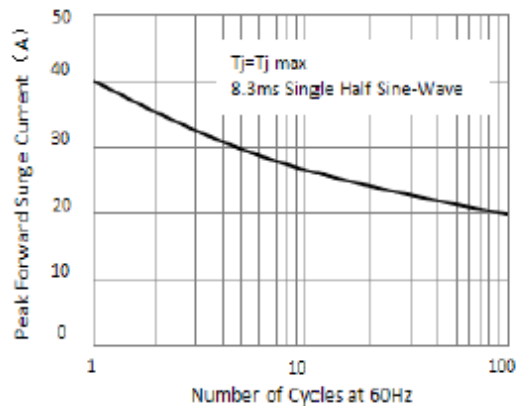


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current

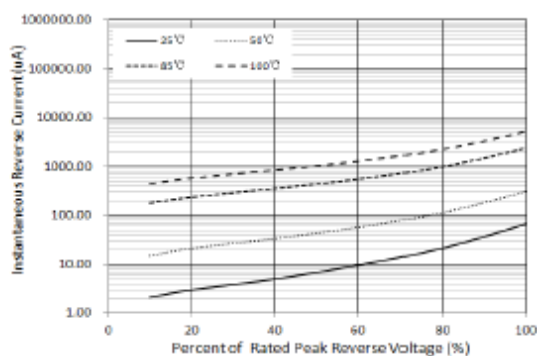


Figure 3. Typical Reverse Characteristics

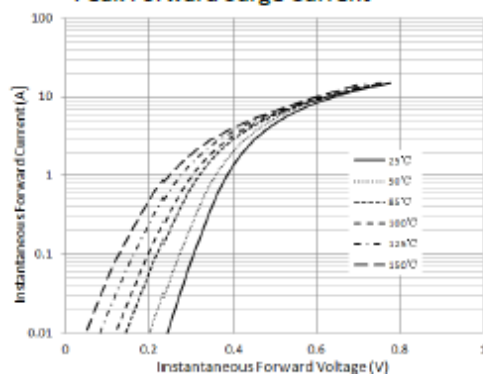


Figure 4. Typical Instantaneous Forward Characteristics

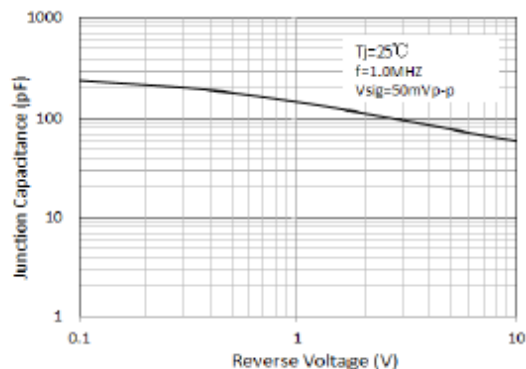
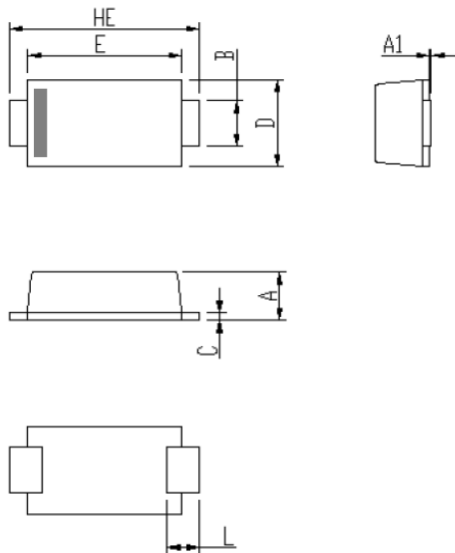


Figure 5. Typical Junction Capacitance

Package Outline Dimensions

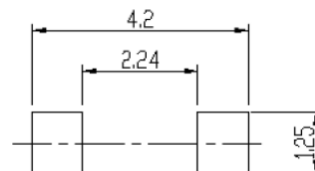
in inches (millimeters)

eSGA (SOD-123FL)



DIM	Unit: mm		Unit: inch	
	MIN	MAX	MIN	MAX
A	0.9	1.08	0.035	0.043
A1	0	0.1	0.000	0.004
B	0.85	1.05	0.033	0.041
C	0.1	0.25	0.004	0.010
D	1.7	2	0.067	0.079
E	2.9	3.1	0.114	0.122
L	0.43	0.83	0.017	0.033
HE	3.5	3.9	0.138	0.154

Soldering footprint



Disclaimers

These materials are intended as a reference to assist our customers in the selection of the Suzhou Good-Ark product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Suzhou Good-Ark Electronics Co., Ltd. or a third party.

Suzhou Good-Ark Electronics Co., Ltd. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.

All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Suzhou Good-Ark Electronics Co., Ltd. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Suzhou Good-Ark Electronics Co., Ltd. or an authorized Suzhou Good-Ark Electronics Co., Ltd. for the latest product information before purchasing a product listed herein. The information described here may contain technical inaccuracies or typographical errors. Suzhou Good-Ark Electronics Co., Ltd. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors. Please also pay attention to information published by Suzhou Good-Ark Electronics Co., Ltd. by various means, including our website home page.

(<http://www.goodark.com>)

When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Suzhou Good-Ark Electronics Co., Ltd. assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.

The prior written approval of Suzhou Good-Ark Electronics Co., Ltd. is necessary to reprint or reproduce in whole or in part these materials.

Please contact Suzhou Good-Ark Electronics Co., Ltd. or an authorized distributor for further details on these materials or the products contained herein.