

GS08D065SW

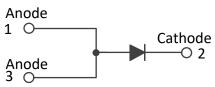
GOOD-ARK Electronics

8A, 650V Silicon Carbide Schottky Diode

Features

- High-Frequency Operation
- Zero Reverse Recovery Current
- Temperature-Independent Switching
- Extremely Fast Switching
- Plastic package has underwriters Laboratory Flammability Classification 94V-0
- Halogen-free according to IEC 61249-2-21

TO-263AB(D²PAK)



Applications

- Boost Diodes in PFC or DC/DC stages
- LED Lighting Power Supplies
- Power Factor Correction

Mechanical Data

- Case: Epoxy, Molded
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 sec
- Shipped 50 units per plastic tube or tape reel packing 800/reel

Maximum Ratings & Electrical Characteristics(TA=25°C unless otherwise noted)					
Parameter	Symbol	GS08D065SW	Unit		
Maximum repetitive peak reverse voltage	Vrrm	650	V		
Working peak reverse voltage	VRWM	650	V		
Maximum DC blocking voltage	VDC	650	V		
	Tc=25°C		26	A	
Maximum average forward rectified current	Tc=135°C	lf(AV)	11.7		
	Tc=152°C		8		
Peak forward surge current, tp=10ms,Half Sine	IFSM	64	А		
Devuer dissinction	Tc=25°C	Der	102	w	
Power dissipation	Tc=110°C	- Ptot	44		
Operating junction temperature range	TJ	-55 to +175	°C		
Storage temperature range	Тѕтс	-55 to +175	°C		



Electrical Specifications(TA=25°C unless otherwise noted)						
Parameter	Symbol	Test Conditions	Тур	Max	Unit	
Forward drop voltage	VF	IF=8A, TJ=25°C	1.40	1.65	v	
Forward drop voltage		I⊧=8A, Tյ=175℃	1.80	2.40		
Poverse leakage current @rated Vp	IR	V _R =650V, TJ=25℃	2	50	μA	
Reverse leakage current @rated VR		V _R =650V, TJ=175℃	10	180		
Total capacitive charge	Qc	Vr=400V, IF=8A, Tj=25°C	28	-	nC	
Total capacitance	С	Vr=400V, Tj=25°C, f=1MHz	42	-	pF	

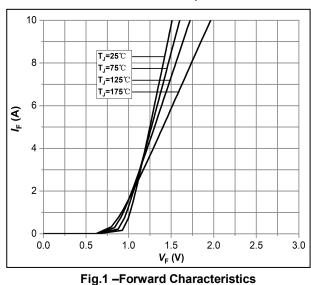
Thermal-Mechanical Specifications (TA=25°C unless otherwise noted)				
Parameter	Symbol	Тур	Max	Unit
Thermal Resistance, Junction to Case	Rejc	1.47	-	°C /W



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Ratings and Characteristics Curves

(TA = 25°C unless otherwise noted)



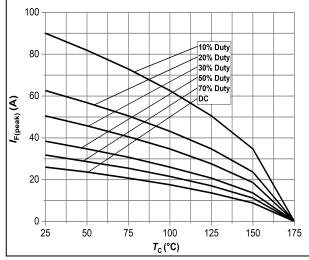


Fig.3 –Current Derating

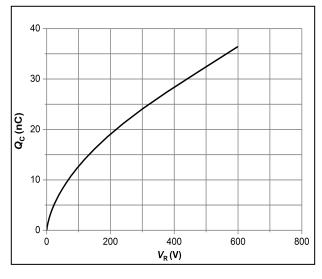


Fig.5 –Total Capacitance Charge vs. Reverse Voltage

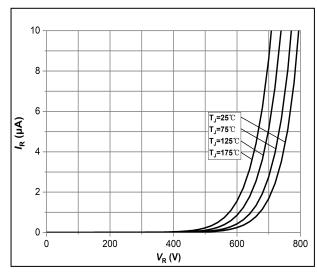


Fig.2 – Reverse Characteristics

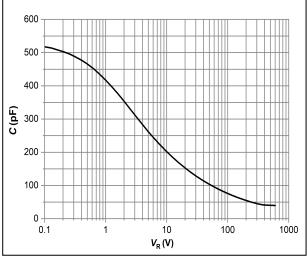


Fig.4 – Capacitance vs. Reverse Voltage

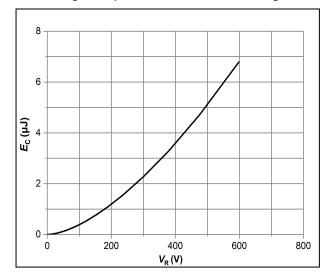
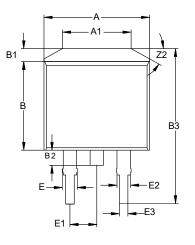


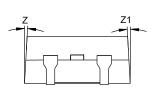
Fig.6 – Typical Capacitance Stored Energy

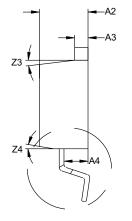


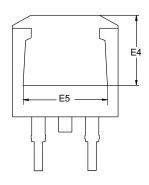
Package Outline Dimensions (Unit: millimeters)

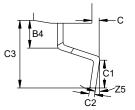
TO-263AB







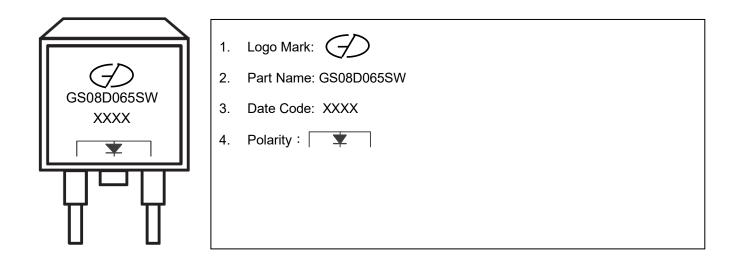




TO-263AB							
	Min.	Nom.	Max.		Min.	Nom.	Max.
А	9.8	10	10.2	C3	5	5.3	5.6
A1	6.5			Е	1.17	1.37	1.57
A2	4.4	4.6	4.8	E1	2.44	2.54	2.64
A3	1.17	1.27	1.37	E2	1.17	1.27	1.37
A4	2.37	2.67	2.97	E3	0.7	0.8	0.9
В	8.5	8.7	8.9	E4	6.47	6.67	6.87
B1	1.07	1.27	1.47	E5	8.3	8.5	8.7
B2	1.2	1.5	1.8	Ζ		3°	
B3	15	15.3	15.6	Z1		3°	
B4	1.8	2	2.2	Z2		30°	
С	0		0.25	Z3		7°	
C1	2.34	2.54	2.74	Z4		7°	
C2	0.3	0.4	0.5	Z5	-4°		4°



Marking Outline



Revision History

Document Version	Date of release	Description of changes
Rev.A	2022.06.16	Preliminary Datasheet



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