

10A, 650V Silicon Carbide Schottky Diode

Features

- High-Frequency Operation
- Zero Reverse Recovery Current
- Temperature-Independent Switching
- Extremely Fast Switching
- Plastic package has underwriters Laboratory Flammability Classification 94V-0
- Halogen-free according to IEC 61249-2-21

Applications

- Boost Diodes in PFC or DC/DC stages
- LED Lighting Power Supplies
- Power Factor Correction

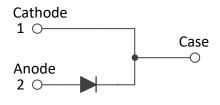
Mechanical Data

- Case: Epoxy, Molded
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 sec
- Shipped 2500 units per reel

| Maximum Ratings & Electrical Characteristics(TA=25°C unless otherwise noted) | | | | | |
|--|----------|-------------|------|---|--|
| Parameter | Symbol | GS10D065SD | Unit | | |
| Maximum repetitive peak reverse voltage | Vrrm | 650 | V | | |
| Working peak reverse voltage | Vrwm | 650 | V | | |
| Maximum DC blocking voltage | VDC | 650 | V | | |
| | Tc=25°C | | 33 | | |
| Maximum average forward rectified current | Tc=135°C | lf(AV) | 15 | А | |
| | Tc=153°C | | 10 | | |
| Peak forward surge current, tp=10ms,Half Sin | IFSM | 70 | А | | |
| Power dissinction | Tc=25°C | Ptot | 75 | w | |
| Power dissipation | Tc=110°C | FIOL | 32 | | |
| Operating junction temperature range | TJ | -55 to +175 | °C | | |
| Storage temperature range | Тѕтс | -55 to +175 | °C | | |



TO-252(D-PAK)





| Electrical Specifications(TA=25°C unless otherwise noted) | | | | | | |
|---|--------|--------------------------------|------|------|---------|--|
| Parameter | Symbol | Test Conditions | Тур | Max | Unit | |
| Forward drop voltone | | IF=10A, TJ=25°C | 1.40 | 1.70 | N | |
| Forward drop voltage | VF | IF=10A, TJ=175°C | 1.65 | 2.20 | V | |
| Poweree lookage ourrent @reted Ve | lr | V _R =650V, TJ=25℃ | 2 | 50 | | |
| Reverse leakage current @rated VR | | V _R =650V, TJ=175°C | 10 | 200 | μΑ) | |
| Total capacitive charge | Qc | VR=400V, IF=10A, TJ=25°C | 36 | - | nC | |
| Total capacitance | С | VR=400V, TJ=25°C, f=1MHz | 52 | - | pF | |

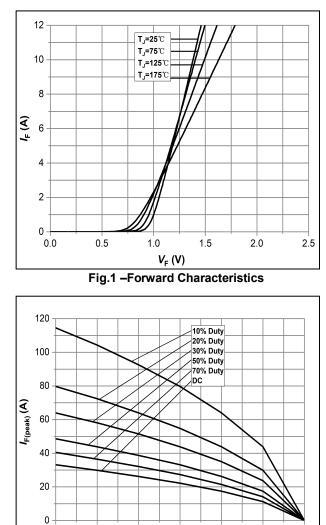
| Thermal-Mechanical Specifications (TA=25°C unless otherwise noted) | | | | | |
|--|--------|-----|-----|-------|--|
| Parameter | Symbol | Тур | Max | Unit | |
| Thermal Resistance, Junction to Case | Rejc | 2.0 | - | °C /W | |

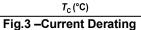


GOOD-ARK Electronics

Ratings and Characteristics Curves

(TA = 25°C unless otherwise noted)





75

100

125

150

175

50

25

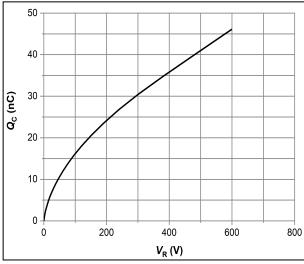
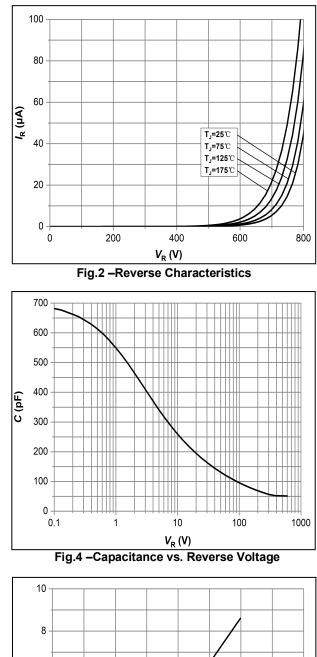


Fig.5 – Total Capacitance Charge vs. Reverse Voltage



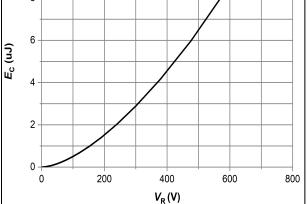
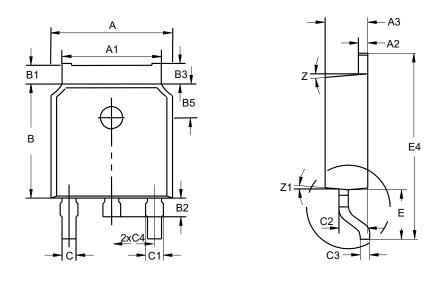


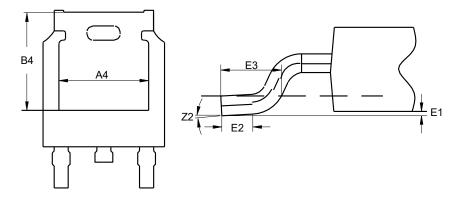
Fig.6 – Typical Capacitance Stored Energy



Package Outline Dimensions (Unit: millimeters)

TO-252 (D-PAK)

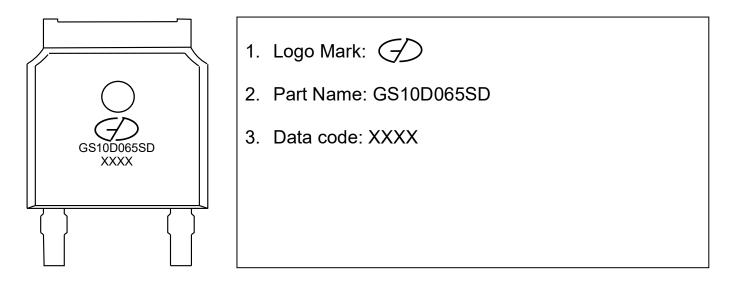




| TO-252 | | | | | | | |
|--------|------|------|------|----|------|------|------|
| | Min. | Nom. | Max. | | Min. | Nom. | Max. |
| А | 6.34 | 6.54 | 6.74 | C1 | 0.65 | 0.85 | 1.05 |
| A1 | 5.1 | 5.3 | 5.5 | C2 | 1.34 | 1.54 | 1.74 |
| A2 | 0.4 | 0.5 | 0.6 | C3 | 0.4 | 0.5 | 0.6 |
| A3 | 2.08 | 2.28 | 2.48 | C4 | 2.09 | 2.29 | 2.49 |
| A4 | 4.6 | 4.8 | 5.0 | Е | 2.6 | 2.9 | 3.2 |
| В | 5.8 | 6.1 | 6.4 | E1 | 0 | | 0.15 |
| B1 | 0.82 | 1.02 | 1.22 | E2 | 0.7 | | |
| B2 | 0.8 | 1 | 1.2 | E3 | 1.3 | 1.6 | 1.9 |
| B3 | 0.9 | 1.1 | 1.3 | E4 | 9.8 | 10.1 | 10.4 |
| B4 | 5.05 | 5.25 | 5.45 | Ζ | | 7° | |
| B5 | 7.83 | 8.03 | 8.23 | Z1 | | 7° | |
| С | 0.56 | 0.76 | 0.96 | Z2 | 0° | | 10° |



Marking Outline



Revision History

| Document Version | Date of release | Description of changes |
|------------------|-----------------|------------------------|
| Rev.A | 2023.01.12 | Preliminary Datasheet |
| | | |
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